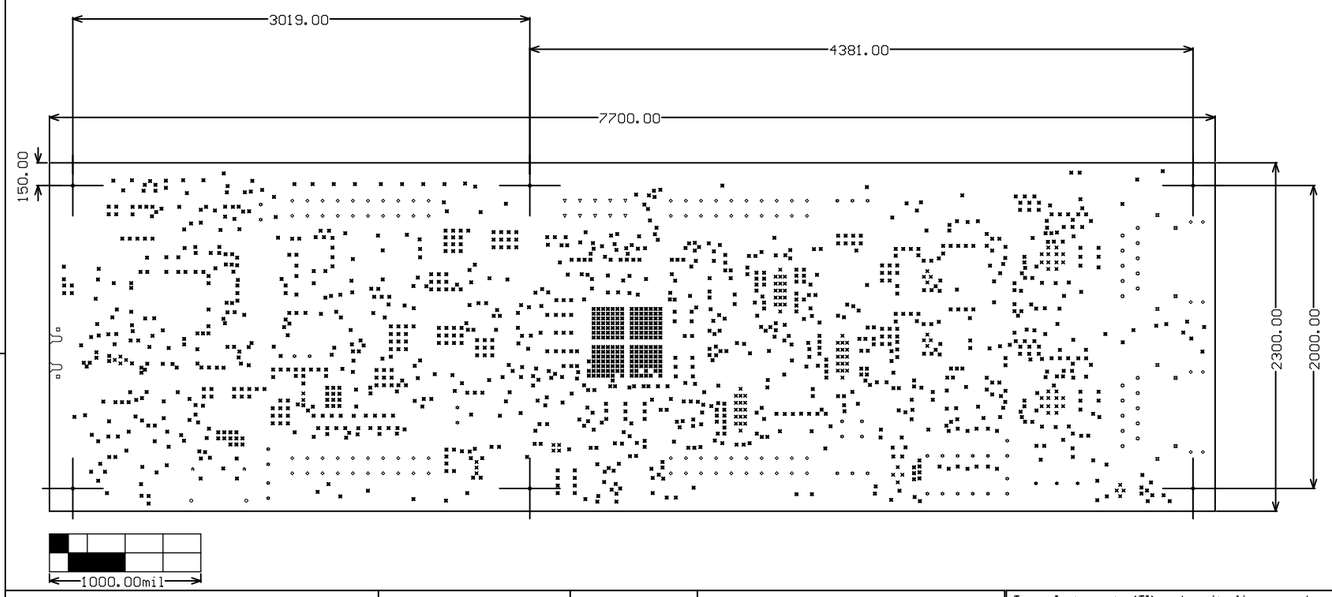
Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.40mil	3.5	
1	Top Layer		1.40mil		
	Dielectric 1	PP-006	2.80mil	4.1	
2	GND 1		1.38mil		
	Dielectric 3	PP-006	2.80mil	4.1	
3	SIG 1		1.38mil		
	Dielectric1	FR-4 High Tg	59.20mil	4.8	
4	SIG 2		1.38mil		
	Dielectric 4	PP-006	2.80mil	4.1	
5	GND 2		1.38mil		
	Dielectric 2	PP-006	2.80mil	4.1	
6	Bottom Layer		1.40mil		
	Bottom Solder	Solder Resist	0.40mil	3.5	
	Bottom Overlay				

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
	6	126.00mil (3.200mm)	NPTH	Round	Top Layer - Bottom Layer	+/-3.00mil
	4	127.95mil (3.250mm)	NPTH	Round	Top Layer - Bottom Layer	
	104	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
	1474	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer	
	20	35.43mil (0.900mm)	PTH	Round	Top Layer - Bottom Layer	
	10	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
	34	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	
	88	40.16mil (1.020mm)	PTH	Round	Top Layer - Bottom Layer	
	3	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
	4	66.93mil (1.700mm)	PTH	Round	Top Layer - Bottom Layer	
	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	2	62.99mil (1.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	2	82.68mil (2.100mm)	PTH	Slot	Top Layer - Bottom Layer	
	1753 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



DESIGN INFORMATION

MIN. TRACK WIDTH: 4 MIL
MIN. CLEARANCE: 4 MIL
MIN. VIA PAD SIZE: 18 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL

MATERIAL:
[ ] FR-408 [X] FR-4 High Tg [ ] OTHER _____
THICKNESS: [X] 62 MIL (1.6mm) +/-10% [ ] OTHER _____
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/- _____

DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH COPPER THICKNESS: [X] 20-30 um [ ] OTHER _____

BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER _____
SOLDER RESIST COLOR: [ ] GREEN [X] OTHER RED
[X] MATTE [ ] SEMI-GLOSS
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER _____
ARRAY/PANEL: [ ] CUT AND TRIM PER M1 BOARD OUTLINE
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] RoHS [ ] OTHER PER ORDER
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS.
PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER

ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER
[ ] XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE
[ ] XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE
[ ] OUTER XX MIL TRACES REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE
[ ] LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE

TEXAS INSTRUMENTS

PROJECT TITLE: AM263x Launchpad
DESIGNED FOR: .PRJ_Customer
FILE NAME: PROC111E2.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PROC111 REV: .PCB_Rev SVN REV: 5a857666cec459008fc5f5fc
LAYER NAME = [illegible]ensions	TID #: N/A
PLOT NAME = Fabrication Drawing	GENERATED : 5/9/2022 12:23:06 PM TEXAS INSTRUMENTS

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ENGINEER:	LAYOUT BY:
a0271760	.PCB_Layout
SCALE: 1.00	ALTIUM DESIGNER VERSION: 22.3.1.43